



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of:

Sitaram YADAVALLI, et al.

Serial No.: 09/531,910

Group Art Unit: 2123

Filed: March 20, 2000

Examiner: H. Day

FOR: METHOD AND APPARATUS FOR MODELING AND
CIRCUITS WITH ASYNCHRONOUS BEHAVIOR

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AMENDMENT AND RESPONSE UNDER 37 C.F.R. § 1.116

APR 27 2004

Technology Center 2100

Commissioner for Patents
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Sir:

In response to the Office Action mailed January 20, 2004, please amend the
above-identified application as follows:

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